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DRIVING EMBEDDED EXCELLENCE



Product :	BR_XETK-S4.0A			
Title :	Release Notes			
File :	BR_XETK-S4_Release-Notes_V06.docx			
TTNR :	F-00K-111-993			
Comments :	Current shipped hardware state: E010/02 Current released firmware version: HSP 12.1.0			
Created:	Name R. Mai	Department PGA/PRM-M2	Signature R. Mai	Date 2020-02-23
Released:	Name K.-H. Nirschl	Department EHE/EAL-PO	Signature K.-H.Nirschl	Date 2020-06-18
Changes				
Revision	Description	Date	Name	Signature
01	Initial Version	2019-06-11	Mai	Mai
02	Improvements and bug fixes with HSP11.14.0	2019-09-17	Mai	Mai
03	Improvements and bug fixes with HSP11.15.0	2019-12-10	Mai	Mai
04	Improvements and bug fixes with HSP12.0.0	2020-03-23	Mai	Mai
05	PCB Change from Rev.D to Rev.E	2020-04-28	Nirschl	Nirschl
06	Improvements and bug fixes with HSP12.1.0	2020-06-18	Nirschl	Nirschl

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1 General Information

1.1 Safety Notice

Calibration activities influence the behavior of the ECU and the systems controlled by the ECU. This may result in unexpected behavior of the vehicle and thus can lead to safety critical situations. Only well trained personnel should be allowed to perform calibration activities.

1.2 System Requirements

The following minimum system prerequisites have to be met:

Required Hardware

- GHz Processor
- GB RAM
- DVD-ROM drive (for installation)
- Network adapter
- Graphics with a resolution of at least 1024x768, 256 MB RAM, 16bit color and DirectX9

Required Operating System

- Windows® 7 SP1 (32 or 64bit*) or higher.
- Windows® 8 (32 / 64 bit*) and Windows® 8.1 (32 / 64 bit*)
- Windows® 10

*) INCA uses the 32bit compatibility mode on a 64-bit operating system.

Required Free Disk Space

- 1 GB (not including the size for user data; absolute min. required, but not recommended)

The following system prerequisites are recommended:

Recommended Hardware

- GHz Quad-Core Processor or equivalent
- 16 GB RAM
- DVD-ROM drive (for installation)
- Network adapter
- Graphics with a resolution of at least 1280 x 1024, 1GB RAM, 32bit color and DirectX9

Recommended Operating System

- Windows® 7 SP1 64bit (INCA uses the 32bit compatibility mode on a 64-bit operating system)

Recommended Free Disk Space

- >10 GB

Recommendation on Performance

- Investigation on performance showed:
 - More Memory improves execution time of repetitive operations
 - SSD hard disks improve the file access times

1.3 Restrictions

WINDOWS® 95b, WINDOWS® NT, WINDOWS® 2000, WINDOWS® 98SE, and WINDOWS® XP are not supported

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1.4 Miscellaneous

To ensure the highest data throughput from the BR_XETK device up to the PC system the following recommendations should be considered:

- Set power save mode to the highest level
- Disable virus scan
- Use network adapter for ETAS application only
- Update network adapter drivers

2 Version Syntax and Tool-Chain Information

2.1 Version-Syntax of the BR_XETK-S4.0A

The BR_XETK-S4.0A hardware version information is located on the product sticker and can be read out of the XETK using the firmware update tool HSP or XETK Configuration Tool.

Hardware State Syntax: **abb/cc**

Description (modification details refer chapter 5)

a	PCB Version (A=V1.0, B=V1.1, C=V1.2, ...)
bb	PCB Hardware State (010, 011, 012, ...)
cc	PCB Population Variant (00, 01, 02, ...)

The BR_XETK-S4.0A Firmware version information can be read out of the XETK using the firmware update tool HSP or XETK Configuration Tool. It is not printed onto a XETK sticker.

Firmware-Version Syntax: **aaa.bbb.ccccc**

Description (modification details refer chapter 5)

aaa	Major Release (0...255)
bbb	Minor Release (0...255)
cccc	Revision/Patch (0...65535)

Firmware Packages:

HDC Work	aaa.bbb.ccccc
Firmware Work	aaa.bbb.ccccc
HDC Rescue	aaa.bbb.ccccc
Firmware Rescue	aaa.bbb.ccccc

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2.2 Version information of the tool-chain components

To get this XETK running with the other components of the tool-chain please make sure that the version mentioned below or a newer one is used. If your software-, firmware- or hardware version is older, please update it.

If you have any problems to get this XETK running please contact our local customer support or sales representative.

Updates or refreshes can be downloaded from the ETAS homepage:

<http://de.etasgroup.com>

<http://en.etasgroup.com>

2.3 Software and microcontroller support

Microcontroller	HSP	INCA	ETK Tools	ASCET-RP	INTECRIO
SR6P7x Cut1	V11.13.0	V7.2.13	V4.1.14	V6.4	V4.6

3 What's New - Release Notes

This chapter lists the main improvements compared to a previous shipped ETK product. Additionally a detailed list of already known issues can be found here.

3.1 New or Enhanced Functions

3.1.1 In INCA 7.3.1 and HSP 12.1.0

Issue Identifier	Description
624438	WGDIS disabled with nSGOff instead of PWOn
ETKX-415	Detect and Report Errors on ECU write access

3.1.2 In INCA 7.3.0 and HSP 12.0.0

Issue Identifier	Description
627517	BDF is not working in some cases
ETKPRG-651	Concurrent XCP debugging and PROF flashing

3.1.3 In INCA 7.2.15 and HSP 11.15.0

Issue Identifier	Description
624438	Align XETK Watchdog disable Pin (WGDIS) behavior
623119	DBG_LLT of XCP debugging not working during PORST
n/a	Initial Support of ECU Info Mailbox

3.1.4 In INCA 7.2.14 and HSP 11.14.0

Issue Identifier	Description
618275	Standby Current optimized
620499	16bit access not correctly executed
619762	Infrequent wrong order of XCP counter between DTO and CTO response
n/a	Further Feature support: <ul style="list-style-type: none"> - SWD Performance Improvement - TimeSynchronization via IEE1588/PTP - RapidPrototyping (SBBv2.1) - MCE - ColdStart Measurement - XCP SW Debugging API

3.1.5 In INCA 7.2.13 and HSP 11.13.0

Issue Identifier	Description
n/a	Initial version, support of BR_XETK-S4

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3.2 Known issues

3.2.1 In INCA 7.2.13 and HSP 11.13.0

Issue Identifier	Description
n/a	Not supported yet: <ul style="list-style-type: none"> - Flashing - TRACE to RAM - IEEE1588 time sync - Rapid Prototyping (Bypass)

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4 Product Variants

The BR_XETK-S4.0A can be purchased in one variant. For details refer to the user guide.

5 Hardware Modifications

5.1 General remarks to this chapter

Hardware issues or obsolete parts can make it necessary to modify the population of the BR_XETK. The first released version, available modifications, and current version are listed below. For the version syntax please refer to chapter 2.1.

5.2 First delivered version

The hardware state **D010/01** is the first delivered version.

5.3 Current delivery condition

The hardware state **E010/02** will be delivered with all new shipments.

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6 Firmware Modifications

6.1 General remarks to this chapter

The programmable logic code within the BR_XETK-S4.0A is stored onto programmable logic devices (FPGA, Firmware). The first released version and current version are listed below. For the version syntax please refer to chapter 2.1.

6.2 First delivered version

FPGA Work	1.0.48
Firmware Work	1.0.59
FPGA Rescue	1.1.30
Firmware Rescue	1.1.1

6.3 Current delivery condition

The following firmware versions will be programmed into all BR_XETK-S4.0A shipments:

FPGA Work	1.3.36
Firmware Work	1.3.39
FPGA Rescue	1.1.30
Firmware Rescue	1.1.1

In case of any problems the above mentioned components can be programmed to the XETK by using HSP V12.1.0. This HSP version is similar to the currently delivered XETK products. Newer HSP versions could contain bug fixes and / or new features.

Attention: For updating the XETK with a later version by using the HSP Firmware update tool, all XETK - packages will be updated one after another and this will last a few minutes. Update of FPGA(rescue) and Firmware(rescue) is **not** done in a failsafe manner.

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7 Abbreviations

ASCET-RP	Rapid Prototyping Software of ETAS
BR_XETK	Product (emulator test probe)
firmware	Software running on the BR_XETK hardware; may be updated for new features or bug fixes
FPGA	F ield P rogrammable G ate A rray; interface component to the application hardware
Hot-fix	Software bug-fix for a refresh version
HSP	H ardware S ervice P ack; ETAS product which includes the firmware for the complete ETAS hardware, shipped together with INCA but also available as standalone product, download at ETAS homepage possible
INCA	Measurement and Calibration Software of ETAS
INTECRIO	Rapid Prototyping Software of ETAS
MC	M easurement & C alibration
PCB	P rinted C ircuit B oard
RP	R apid P rototyping
SBB	S ervice B ased B ypass
tool-chain	MC hardware (e.g. ES690) and software (e.g. INCA)
XETK Configuration Tool	Configuration Software, in order to configure a (X)ETK / FETK